

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Dennis CIPLICKAS et al.

Application No.: 10/541,076

Confirmation No.: 4078

Filed: June 29, 2005

Art Unit: 2825

For: DESIGNING AN INTEGRATED CIRCUIT TO
IMPROVE YIELD USING A VARIANT
DESIGN ELEMENT (AMENDED)

Examiner: V. Siek

AMENDMENT UNDER 37 C.F.R. 1.111

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the non-final Office Action dated January 16, 2008, (Part of Paper No./Mail Date 20080103), for which a response is due on April 16, 2008. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.